

# **OMS® v5.0.1 LPWAN SPLITTING MODE (UL-S1)**

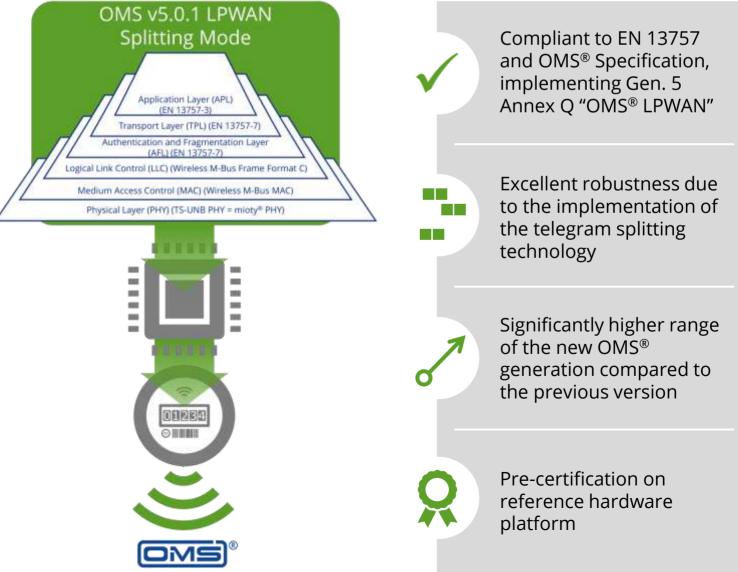
OMS<sup>®</sup> v5 - the new generation of the OMS<sup>®</sup> stack is making its way into the metering world. With this generation you get a powerful product that merges the OMS<sup>®</sup> specification with the Physical Layer of the mioty<sup>®</sup> LPWAN technology, specified according to TS-UNB - ETSI TS 103357.

Use this stack to introduce your OMS® customers to a product that is not only

OMS v5.0.1 LPWAN Splitting Mode pplication Layer (APL) (EN 13757-3) Transport Layer (TPL) (EN 13757-7) Authentication and Fragmentation Layer (AFL) (EN 13757-7) Logical Link Control (LLC) (Wireless M-Bus Frame Format C Medium Access Control (MAC) (Wireless M-Bus MAC) Physical Layer (PHY) (TS-UNB PHY = mioty® PHY) 

characterized by excellent robustness thanks to the integration of telegram splitting technology, but also has a significantly greater range than previous OMS<sup>®</sup> versions.

With the new OMS® generation, you can offer your customers high-performance products with the option of expanding their fields of application in their existing OMS<sup>®</sup> networks.



# SUPPORTED SPECIFICATIONS

### Compliant according

- EN 13757 and OMS<sup>®</sup> Specification, implementing Gen. 5 Annex Q "OMS<sup>®</sup> LPWAN"
- TS-UNB ETSI TS 103357

## **STACK FEATURES**

- Unidirectional transmission [UL-S1] in asynchronous mode
- OMS<sup>®</sup> security profiles A & B
- Short and long TPL header (CI-fields 7Ah and 72h)
- Physical parameters [UL-S1], according to Telegram Splitting Ultra Narrow Band (TS-UNB) profile EU0

## **REFERENCE HARDWARE**

STM32L072CZ + SX1276

verified on B-L072Z-LRWAN1

# **STACK CHARACTERISTICS**

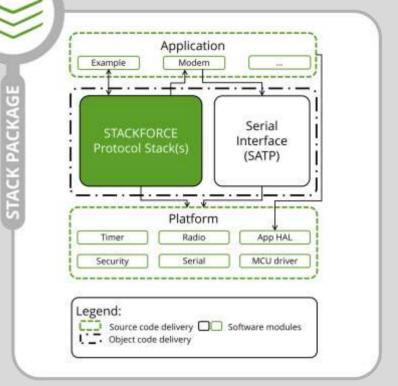
#### Memory Requirements\*

	Library	Applications, HAL, Drivers, other software
Flash	~ 23 kB	~ 33 kB
RAM	~ 2,9 kB	~ 1,3 kB

• Already included **400 bytes RAM** to typical stack library due to dynamic memory allocation (\_RAM).

\* The code sizes described above specify the typical required memory for operating the full featured protocol stack as a library including related drivers. Values based on reference hardware.

# STACK PACKAGE ARCHITECTURE



# **YOUR BENEFITS**



The OMS<sup>®</sup> v5 stack generation enables a high degree of device interoperability.



Combination of the interoperability of OMS<sup>®</sup> applications and the great robustness and range of mioty <sup>®</sup>.



Platform Interface enabling customization and fine tuning of Platform Driver.



With a directly available OMS<sup>®</sup> v5.0.1 Standard Stack you shorten your time-to-market significantly.



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